

09/915761 *cofc*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent No. 6,944,748 B2)
SANCHES ET AL.)

Issued: SEPTEMBER 13, 2005)

For: SIGNAL PROCESSOR EXECUTING)
VARIABLE SIZE INSTRUCTIONS)
USING PARALLEL MEMORY BANKS,)
AND CORRESPONDING METHOD)

Certificate
FEB 13 2006
of Correction

REQUEST FOR CERTIFICATE OF CORRECTION OF
APPLICANT'S MISTAKE UNDER RULE 1.323

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of Rule 1.323 of the Rules of Practice, the Patent Office is respectfully requested to correct matters in the above-identified patent in accordance with the Certificate of Correction Form PTO-1050 attached hereto.

The Patent Office is requested to issue a Certificate of Correction on the above-referenced Patent Document, and to place such a Certificate of Correction in the file, so that such will appear on any copies of the patent which are ordered in the future.

The corrections being made are of consequence to provide the proper recitation in the specification and claims. Some of the mistakes occurred in good faith and are not the fault of the Patent and Trademark Office. However, several printing errors of the Patent Office are also being corrected. No new matter is added by these corrections.

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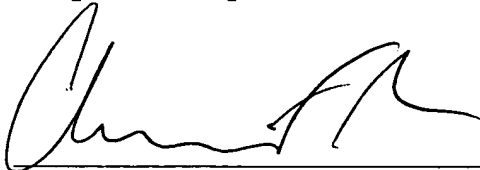
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FEB 13 2006

In re Patent of
SANCHES ET AL.
Patent No. 6,944,748 B2
Issued: **SEPTEMBER 13, 2005**

Authorization is given to charge the requisite fee as set forth in 37 CFR § 1.20(a) in the amount of \$100.00 to the credit card listed on the attached credit card payment form PTO-2038. The Commissioner is authorized to charge or credit any discrepancies in fee amounts to Deposit Account 01-0484.

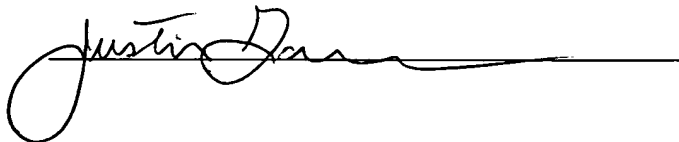
Respectfully submitted,



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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450, on this 31st day of January, 2006.



FEB 13 2006

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO.	:	6,944,748 B2
DATED	:	September 13, 2005
INVENTOR(S)	:	Sanches et al.

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below

Column 1, Line 39	Delete: "UE1" Insert: --EU1--
Column 2, Line 11	Delete: "is object" Insert: -- is the object--
Column 3, Line 38	Delete: "when i less" Insert: --when i is less--
Column 4, Line 34	Delete: "each to the" Insert: --each of the--
Column 9, Line 15	Delete: "c'(0)-c'(3)" Insert: -- c(0)-c(3)--
Column 12, Line 38	Delete: "else val(j-1)=v;" Insert: --else val(j-1)=v;--
Column 13, Line 51	Delete: "else val(j-1)=v;" Insert: --else val(j-1)=v;--
Column 14, Line 6	Delete: "bank;" Insert: --banks;--
Column 14, Line 40	Delete: "after reorganization c(ix)" Insert: --after reorganization, c(ix)--
Column 14, Line 41	Delete: "before reorganization c'(j)" Insert: --before reorganization, c'(j)--

MAILING ADDRESS OF SENDER:

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Column 14, Line 61	Delete: "else val(j-1)=v;" Insert: --else val(j-1)=v;--
Column 15, Line 11	Delete: "nemory" Insert: --memory--
Column 16, Line 10	Delete: "end" Insert: --and--
Column 16, Line 13	Delete: "claim 22, the" Insert: --claim 22, wherein the--
Column 16, Line 28	Delete: "else val(j-1)=v;" Insert: --else val(j-1)=v;--

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